

OCT 09 2003

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PTO/SB/21 (03-03)

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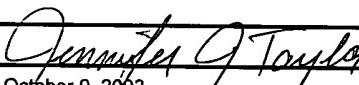
Total Number of Pages in This Submission

Application Number	10/092,875
Filing Date	March 5, 2002
First Named Inventor	Paul A. Morgan
Art Unit	2829
Examiner Name	Kilday, Lisa A.
Attorney Docket Number	MI22-1798

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to a Technology Center (TC)
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual	Jennifer J. Taylor, Ph.D.; Reg. No. 48,711; Wells St. John P.S.
Signature	
Date	October 9, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/092,875
Confirmation No. 9981
Filing Date March 5, 2002
Inventor..... Paul A. Morgan et al.
Assignee..... Micron Technology, Inc.
Group Art Unit..... 2829
Examiner Kilday, Lisa A.
Attorney's Docket No. MI22-1798
Customer No. 021567
Title: Etching Processes for Integrated Circuit Manufacturing Including Methods of
Forming Capacitors

RESPONSE TO JULY 9, 2003 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
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AMENDMENTS

In response to the Office Action of July 9, 2003, applicant amends and remarks as follows.

EV318285669

In the Specification

NONE